# Máquina de 4 direcciones

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| COD OP | dirOP1 | dirOP2 | dirRES | dirProxInstr |

A = (B + C) x D

E

|  |  |
| --- | --- |
|  | Programa |
| I0 | ADD dirB, dirC, dirA, dirI3 |
| I1 | HALT |
| I2 | DIV dirA, dirE, dirA, dirI1 |
| I3 | MUL dirA, dirD, dirA, dirI2 |

# Máquina de 3 direcciones

|  |  |  |  |
| --- | --- | --- | --- |
| COD OP | dirOP1 | dirOP2 | dirRES |

A = (B + C) x D

E

CPU necesita registro PC para ejecución secuencial

## Programa

I0 ADD dirB, dirC, dirA

I1 MUL dirA, dirD, dirA

I2 DIV dirA, dirE, dirA

I3 HALT

# Máquina de 2 direcciones

|  |  |  |
| --- | --- | --- |
| COD OP | dirOP1/dirRES | dirOP2 |

A = (B + C) x D CPU con registro PC y nuevas operaciones

E

|  |  |
| --- | --- |
|  | Programa |
| I0 | MOV dirA, dirB |
| I1 | ADD dirA, dirC |
| I2 | MUL dirA, dirD |
| I4 | DIV dirA, dirE |
| I5 | HALT |

# Máquina de 1 dirección

|  |  |
| --- | --- |
| COD OP | dirOP2 |

A = (B + C) x D

E

CPU con registro PC y registro Acumulador y nuevas operaciones

## Programa

|  |  |
| --- | --- |
| I0 | LOAD dirB |
| I1 | ADD dirC |
| I2 | MUL dirD |
| I4 | DIV dirE |
| I5 | STORE dirA |
| I6 | HALT |

Máquina de 0 direcciones

### COD OP

CPU con registro PC, Pila y

A = (B + C) x D nuevas operaciones

|  |  |  |
| --- | --- | --- |
| E |  | Programa |
|  | I0 | PUSH dirE |
|  | I1 | PUSH dirD |
|  | I2 | PUSH dirC |
|  | I3 | PUSH dirB |
|  | I4 | ADD |
|  | I5 | MUL |
|  | I6 | DIV |
|  | I7 | POP dirA |
|  | I8 | HALT |